

# Best Paper Candidates/Award Committee

## Best Paper Award Committee

**Karam Chatha**  
*Arizona State Univ.*  
Phoenix, AZ

**Chris Chu**  
*Iowa State Univ.*  
Ames, IA

**Jennifer Dworak**  
*Brown Univ.*  
Providence, RI

**Puneet Gupta**  
*Univ. of California, Los Angeles*  
San Diego, CA

**Chandramouli Kashyap**  
*Intel Corp.*  
Hillsboro, OR

**Xin Li**  
*Carnegie Mellon Univ.*  
Pittsburgh, PA

**Sung Kyu Lim**  
*Georgia Institute of Technology*  
Atlanta, GA

**Gi-Joon Nam**  
*IBM Corp.*  
Austin, TX

**Arijit Raychowdhury**  
*Intel Corp.*  
Portland, OR

**Sherief Reda**  
*Brown Univ.*  
Providence, RI

**L. Miguel Silveira**  
*INESC ID/IST - TU Lisbon*  
Lisboa, Portugal

**Mircea R. Stan**  
*Univ. of Virginia*  
Charlottesville, VA

**Bill Swartz**  
*InternetCAD.com, Inc.*  
Dallas, TX

## IEEE/ACM William J. McCalla ICCAD Best Paper Award Candidates

### Monday Sessions

#### 1A.1 FIDELITY METRICS FOR ESTIMATION MODELS

**Haris Javaid**, Aleksander Ignjatovic, Sri Parameswaran - *Univ. of New South Wales*

#### 2B.1 SETS: STOCHASTIC EXECUTION TIME SCHEDULING FOR MULTICORE SYSTEMS BY JOINT STATE SPACE AND MONTE CARLO

**Nabeel Iqbal**, Jörg Henkel - *Karlsruhe Institute of Technology*

#### 3C.1 YIELD ENHANCEMENT FOR 3-D-STACKED MEMORY BY REDUNDANCY SHARING ACROSS DIES

Li Jiang, Rong Ye, **Qiang Xu** - *The Chinese Univ. of Hong Kong*

### Tuesday Sessions

#### 4B.1 GENERALIZED NONLINEAR TIMING/PHASE MACROMODELING: THEORY, NUMERICAL METHODS AND APPLICATIONS

**Chenjie Gu**, Jaijeet Roychowdhury - *Univ. of California, Berkeley*

#### 5A.1 SELECTIVE INSTRUCTION SET MUTING FOR ENERGY-AWARE ADAPTIVE PROCESSORS

**Muhammad Shafique**, Lars Bauer, Jörg Henkel - *Karlsruhe Institute of Technology*

#### 7C.1 NATIVE-CONFLICT-AWARE WIRE PERTURBATION FOR DOUBLE PATTERNING TECHNOLOGY

**Szu-Yu Chen**, Yao-Wen Chang - *National Taiwan Univ.*

### Wednesday Sessions

#### 8A.1 BI-DECOMPOSITION OF LARGE BOOLEAN FUNCTIONS USING BLOCKING EDGE GRAPHS

**Mihir Choudhury**, Kartik Mohanram - *Rice Univ.*

#### 8B.1 OBSTACLE-AVOIDING RECTILINEAR STEINER MINIMUM TREE CONSTRUCTION: AN OPTIMAL APPROACH

**Tao Huang**, Evangeline F.Y. Young - *The Chinese Univ. of Hong Kong*

#### 9A.1 SIMPL: AN EFFECTIVE PLACEMENT ALGORITHM

**Myung-Chul Kim**, Dong-Jin Lee, Igor L. Markov - *Univ. of Michigan*

#### 9B.1 CHARACTERIZING THE LIFETIME RELIABILITY OF MANYCORE PROCESSORS WITH CORE-LEVEL REDUNDANCY

Lin Huang, **Qiang Xu** - *The Chinese Univ. of Hong Kong*